

FIG. 1

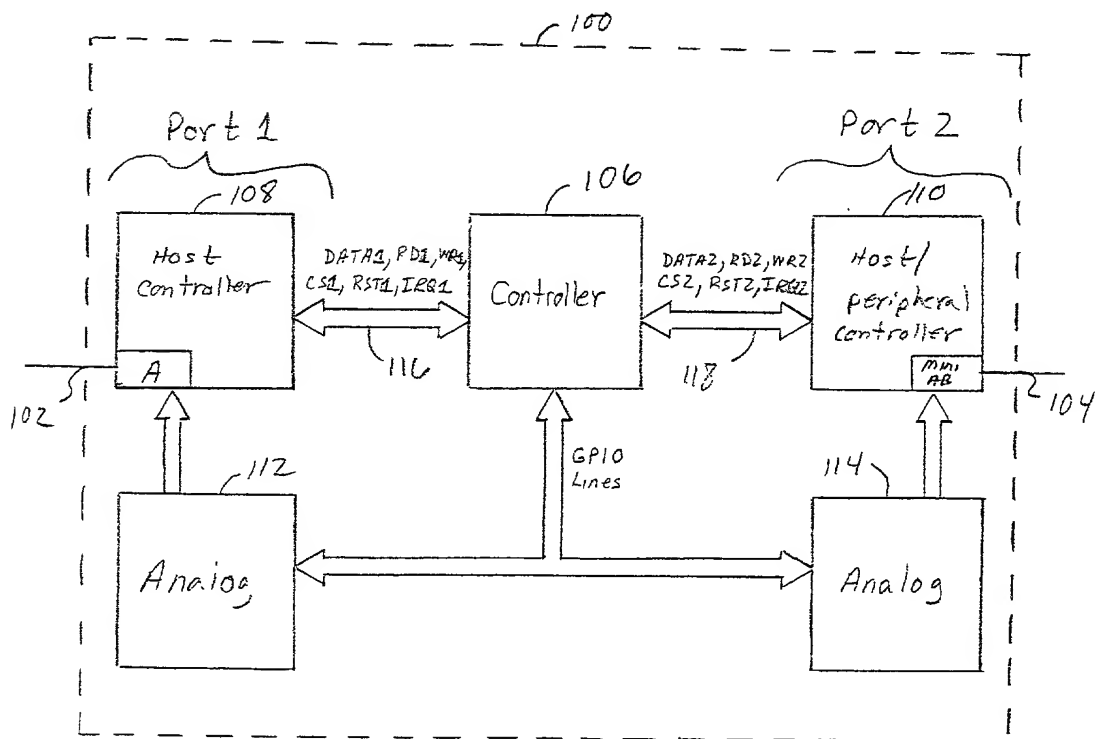


FIG. 2

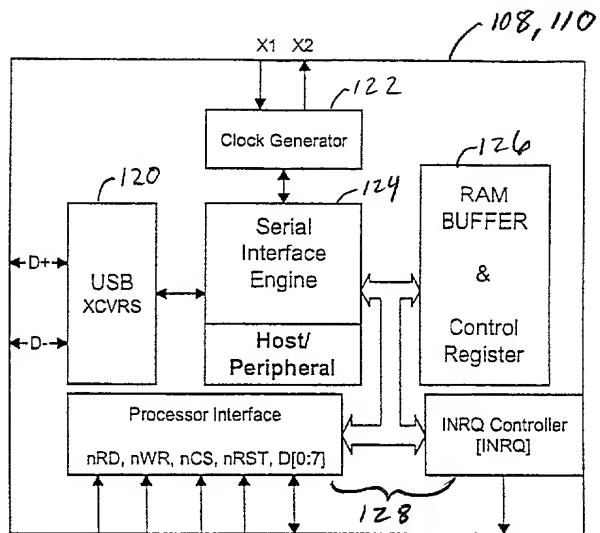


FIG. 3

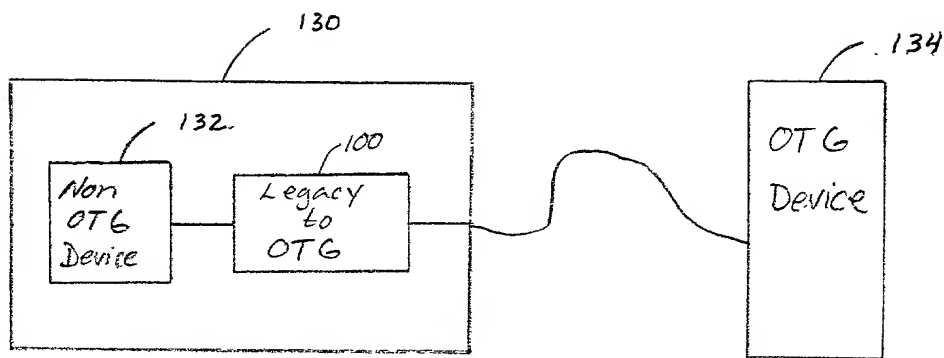


FIG. 4

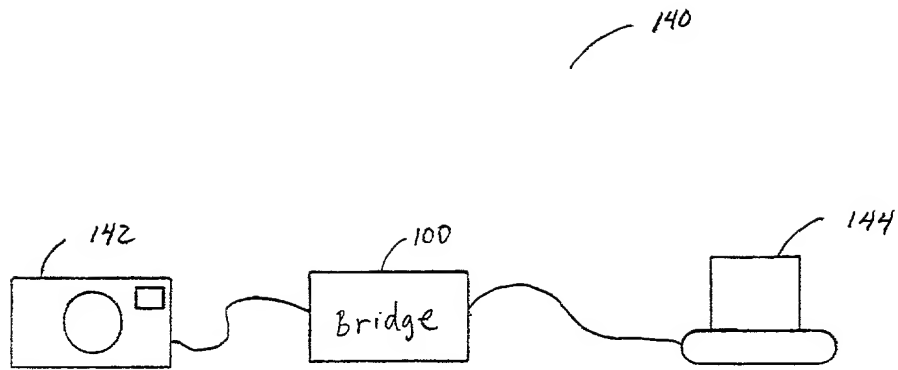


FIG. 5

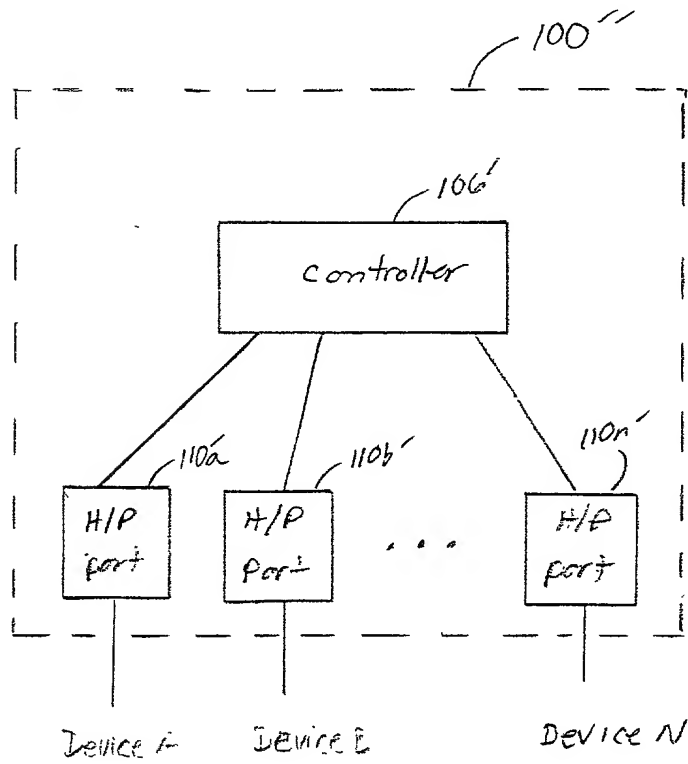


FIG. 6

Host Operation

150

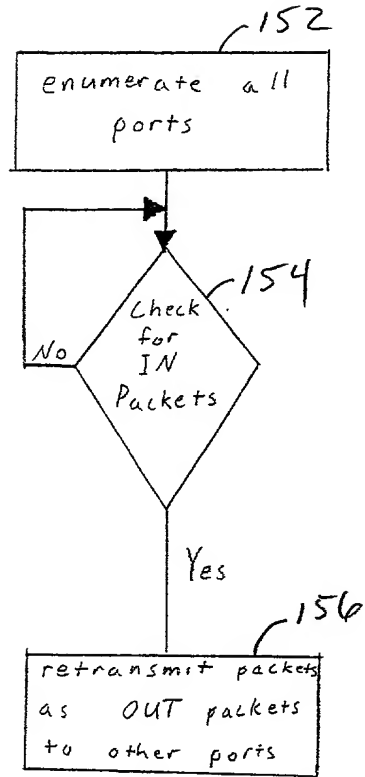


FIG. 7

Peripheral
Operation

160

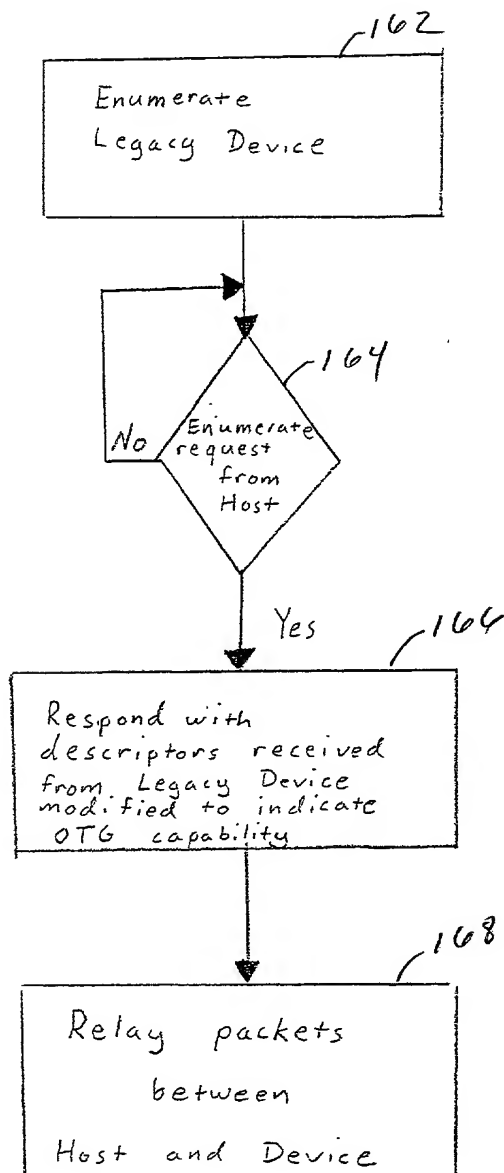


FIG. 8

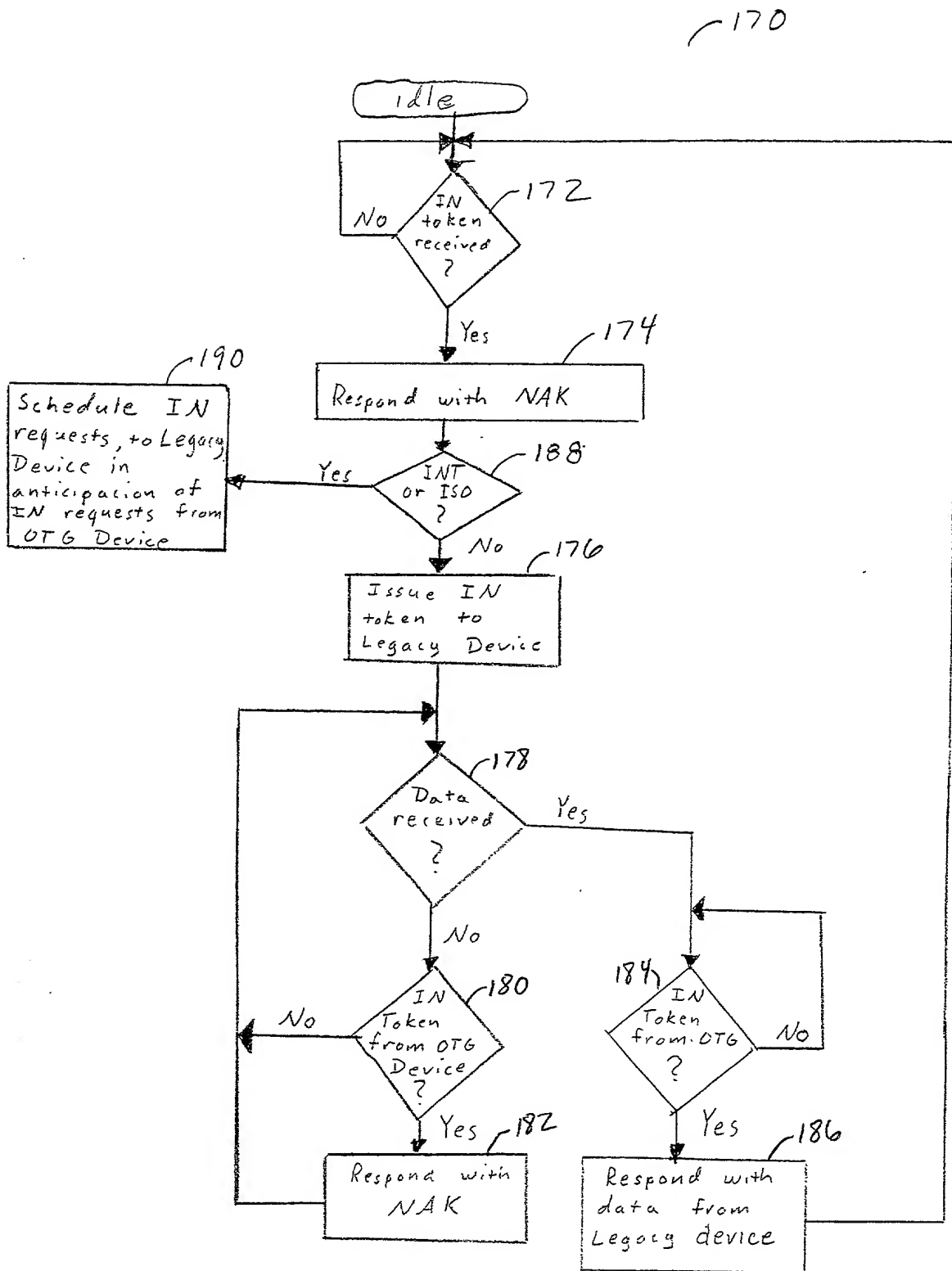


FIG. 9